

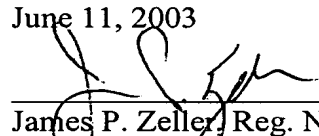


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Expedited Procedure
Art Unit 2822
PATENT

Attorney Docket No. 29936/38063

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kim <i>et al.</i>)	I hereby certify that this paper is being
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Filed: December 28, 2001)	Mail Stop AF, Commissioner for
)	Patents, P.O. Box 1450, Alexandria,
For: Method Of Forming Wiring In)	VA 22313-1450 on this date:
Semiconductor Devices)	
)	June 11, 2003
Group Art Unit: 2822)	
)	James P. Zeller, Reg. No. 28,491
Examiner: Maria F. Guerrero)	

**SUPPLEMENTAL RESPONSE TO FINAL REJECTION
UNDER 37 C.F.R. § 1.116 AND
SUBMISSION OF CORRECTED FORMAL DRAWINGS**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is a supplemental response to the final official action dated June 3, 2003, which addresses the examiner's objections to the drawing figures.

Submitted herewith are three sheets (FIGS. 1A-4D) of corrected, formal drawing figures for filing in the above-identified patent application.

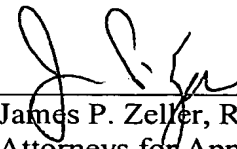
Favorable consideration of the enclosed drawing figures is respectfully requested.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN

June 11, 2003

By:


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